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Final Manufacturing Process of Front Side Metallisation on Silicon Solar Cells Using Conventional and Unconventional Techniques

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The paper presents the results of the investigation of the front electrode manufactured using two silver pastes (PV 145 manufactured by Du Pont and another based on nanopowder experimentally prepared) on monocrystalline silicon solar cells in order to reduce contact resistance. The aim of the paper was a comparison between a conventional and an unconventional method to improve the quality of forming electrodes of silicon solar cells. The Screen Printing (SP) method is the most widely used contact formation technique for commercial silicon solar cells. Selective Laser Sintering (SLS) is a modern manufacturing technique, which uses a high power CO_2 laser to melt or sinter metal powder particles into a mass that has the desired three-dimensional shape, in precisely defined areas. The whole process is controlled by a program that is used for micro-processing. An innovative aspect of this method is the application of a nano silver paste to create the seed layer of the front electrode using the SLS method.

The topography of both co-fired in the infrared belt furnace and melted/sintered in the Eosint M250 Xtended device equipped with CO2 laser were studied. Contacts were investigated using confocal laser scanning microscope (CLSM 5) and a scanning electron microscope (SEM) with an energy dispersive X-ray (EDS) spectrometer for microchemical analysis. Both surface topography and a cross section of the front electrodes were studied using SEM microscope. Phase composition analyses of the chosen front electrodes were done using the XRD method delineated in this paper. Front electrodes were formed on the surface with a different morphology from the solar cells. The median size of the pyramids was measured using the atomic force microscope (AFM). Resistance of the front electrodes was measured using the Transmission Line Model (TLM).

Keywords: electrical properties, solar cell, selective laser sintering, screen printing

O INTRODUCTION

The main focus of this paper was to study the manufacture of the front side metallization of solar cells. Many different methods have been applied to improve the electrical properties of front metallization. The photovoltaic market for silicon solar cells has been growing strongly over the past few years. Scientists are pushed to develop quicker some methods and technologies by the growth, at the same time, achieving both an improvement in efficiency and a reduction in the production costs of photovoltaic cells. Metallization is one of the key process steps in the manufacture of high efficiency solar cells. Fig. 1 presents some methods of manufacturing metal

contacts at the front and back surfaces of solar cells [1] to [5].

The aim of the paper was a comparison of a conventional and an unconventional method to improve the quality of forming electrodes on silicon solar cells. The Screen Printing (SP) method is the most widely used contact formation technique for commercial silicon solar cells. Screen printing is performed on both the front and back sides of solar cell (Fig. 2). Each of the screen printing processes can be divided into three major steps [4] to [6]:

- 1. Overprinting a collection of back contacts (Al/Ag) and drying,
- 2. Overprinting a front electrode (Ag) and drying,
- 3. Co-firing both front and back metal contacts.

FRONT METAL CONTACT	BACK METAL CONTACT	FRONT AND BACK METAL CONTAC	
Laser pad welding	Laser fired contact	Screen printing	
Selective laser sintering	Emitter wrap through	Photolithography	
Light-induced electroplating	Metal wrap through	Single and double sided burie	
Metal aerosol jetting	Metallisation wrap around	contacts	
Pad printing	Interdigitated back contact		
Physical vapour deposition			

Fig. 1. Methods of producing electrical contacts on solar cells [4] and [5]

Selective Laser Sintering (SLS) is a modern manufacturing technique, which uses a high power CO₂ laser to melt or sinter metal powder particles into a mass that has a desired three-dimensional shape in precisely defined areas. The whole process is controlled by a program that is used for microprocessing. Process technology for the generation of front contacts by SLS is shown in Fig. 3 [4] and [5].

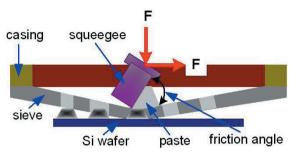


Fig. 2. The screen printing method [4] to [6]

1 EXPERIMENTAL PROCEDURE

The studies were carried out on monocrystalline silicon wafers produced by Deutsche Solar AG Germany. The material properties of the silicon used in this paper are presented in Table 1. The SLS method was used on wafers with a thickness $\sim 330 \, \mu m$, whereas the SP method and co-firing in the furnace was used on wafers with a thickness of $\sim 230 \, \mu m$.

The silver powder with the following granulation: 70, 60 and 40 nm was applied during the preliminary investigations in order to select both the optimal granulation and the powder. Finally, the nanopowder was applied in investigations based on metallographic observations of front electrodes manufactured using the SLS method. The selection of elements in the contact layer of the solar cell is presented in Table 2. The selection of the chemical composition of the

front contact was carried out by experiment and mixtures were prepared using a mechanical mixer. The technology used to produce the solar cell was developed in the Institute of Metallurgy and Materials Science in Krakow, Poland. The process sequence for manufacturing solar cells into different surface morphologies of silicon (100) is presented in Fig. 4.

Table 1. Basic properties of silicon

Туре	р	р
Doped	boron	boron
Thickness	$200 \pm 30 \mu{\rm m}$	$330 \pm 10 \mu \text{m}$
Area	5 x 5 cm	5 × 5 cm
Resistivity	1 ÷ 3 Ω•cm	~1 Ω•cm

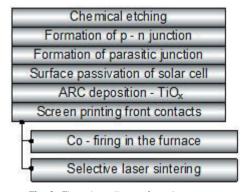


Fig. 4. The solar cell manufacturing process

The chemical procedure for cleaning the wafers is presented in Table 3. Emitter diffusion is one of the crucial steps in the manufacturing process of solar cells. The CVD method was used to apply the donor source. The emitter was generated at 840 °C for 40 minutes in an open quartz tube using POC13 as the doping source. The parasitic junction was removed by means of a special teflon clamp in which the

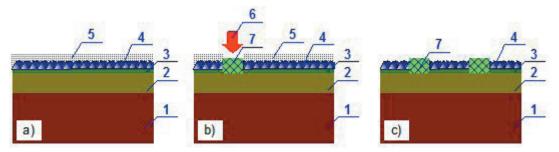


Fig. 3. Technology for the formation of front contacts by SLS: a) a metallic powder is coated on the wafer, b) a laser beam heats the powder locally and melts metal lines on the top of the wafer, c) the excess metallic powder is moved into the container (1 – p type base material, 2 – n type emitter, 3 – dielectric layer, 4 – textured layer, 5 – powder layer, 6 – passage of focus laser beam, 7 – SLS seed layers on the silicon surface) [4] and [5]

silicon wafers were stacked using surface EVA foil separation.

Then the immersed clap was in 27%HF:45%HNO3:27%CH3CO-OH solution in the volume ratio 3:5:3 for 40 seconds, followed by rising in DIH₂O. After diffusion, the wafers were covered by phosphorous-silicate glass (xSiO₂·yP2O₅), which was removed by immersion in a bath of 10% HF for two minutes. Surface passivation (SiO₂) took place at 800 °C for 10 minutes in a controlled atmosphere containing both O₂ and N₂. Titanium dioxide (TiO_x) as an antireflection coating was deposited by spraying at 300 °C with tetraethylorthotitanat ((C₂H₅O)₄Ti) using purified air as a carrier gas. The important aspect of this paper is the fabrication of the front metallization (the test electrodes system). This front metallization (test electrodes system) was manufactured in order to describe the usefulness of silver pastes using two technologies and to evaluate the contact resistance of the metal-semiconductor junction. Two special test electrode systems were prepared by screen printing method (Fig. 5.):

- I sizes of the front paths were: 2 × 10 mm (wide x length), distances between them were: 20, 10, 5 and 2.5 mm,
- II sizes of front paths were: 5 × 10 mm (wide × length), distances between them were: 1, 2, 4 and 8 mm

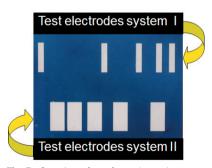


Fig. 5. Overview of test front electrode system

Many initial series for testing the solar cells were first prepared by laser micro-treatment in order to select conditions for the tests (the feed rate of passage of the laser beam and laser beam) and achieve the smallest resistance value of the connection zone between the electrode and the silicon substrate of the solar cell and a uniform structure. The most advantageous conditions of laser micro-treatment were selected based on the tests and metallographic observations. Table 4 shows the two chosen initial series of solar cell testing, which were then prepared by laser micro-treatment. Then laser micro-treatment

conditions were selected for the test electrodes system I, II (Table 5) based on the results of the metallographic observations. Table 6 presents the general device data. In the case of co-firing in the conveyor belt IR furnace (Fig. 6), some solar cells were prepared using the test electrodes system I, II (Table 7). The belt IR furnace was equipped with fitted tungsten filament lamps, heating both the top and bottom of the belt.

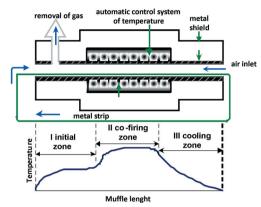


Fig. 6. A sketch of belt IR (an example) [7]

Table 2. Paste properties

Paste	Mass con (prep	Solar cells				
symbol	Basic nanopowder	Organic carrier	Ceramic glaze	 with different morphology** 		
UNCONVECTIONAL METHOD						
А	83	15	2	1, 2, 3, 4		
PV145*	-	-	-	1, 2, 3, 4		
CONVECT	CONVECTIONAL METHOD					
В	85	15	-	1, 2		
С	88.40	11.60	-	3, 4		
PV145*	-	-	-	1, 2, 3, 4		

- **Where:
- 1. Non-textured surface with deposited TiO_x coating;
- 2. Non-textured surface without deposited TiO_x coating;
- 3. Textured surface with deposited TiO_x coating;
- 4. Textured surface without deposited TiOx coating;
- * commercial paste manufactured by the Du Pont company

Table 3. Chemical preparation of silicon wafers

Chemical operations	Chemical recipe
Washing in acetone	CH3COCH3
Rinsing	DIH20
Removal of distorted layer	30% KOH
Rinsing	DIH20
Removal of metallic contamination	2%HCI
Removal of native oxide	10%HF
Rinsing	DIH20

Table 4. Initial conditions of laser micro-treatment for the test electrode system I, II of silicon wafers

Series	Solar cell surface	Paste symbol	Feed rate of passage of laser beam (v), [mm/s]	Laser beam, (P) [W]	Thickness of printed front electrode by screen printing method [µm]
1	Chemically	PV 145	50 to 200	8.1 to 37.8	15, 40, 60
2	cleaned	A	50 to 200	21.5 to 48.6	15, 35, 70

Table 5. Conditions of laser micro-treatment testing electrodes of silicon solar cells with test electrode system I, II

Series	Solar cells with different morphology*	Paste symbol	The thickness of printed front electrode by screen printing method, $[\mu m]$	The feed rate of passage the laser beam (v) [mm/s]	Laser beam (P) [W]
1	1, 2, 3, 4	А	15, 35	37.8, 40.5, 43.2	50
2	1, 2, 3, 4	Α	15, 35	48.5, 51.3, 54	100

^{*}Where: 1. non textured surface with deposited TiO_x coating; 2. non textured surface without deposited TiO_x coating; 3. Textured surface with deposited TiO_x coating; 4. Textured surface without deposited TiO_x coating

The following investigations were performed in this paper:

- The contact resistance R_c , specific contact resistance pc, transfer length (L_T) of front contact solar cell, and the use of the Transmission Line Model (TLM) method for the measuring position were determined at the Institute of Engineering Materials and Biomaterials.
- The topography of the silicon wafer with texture and antireflection coating (ARC) (this increases the absorption of sunlight and protects against pollution) were determined using an atomic force microscope (Park Systems XE 100) with an uncontacted trybe. The median size of the pyramids was also measured using this microscope.
- Phase composition analysis of the chosen front contacts using the XRD method.
- Microchemical analysis of the front chosen contacts using a scanning electron microscope equipped with an energy dispersive X-ray (EDS) spectrometer.
- The topography of both the surface and cross section of front contacts using:
 - Zeiss Supra 35 scanning electron microscope using secondary electron detection with accelerating voltage in the range 5 to 20 kV.
 - Zeiss confocal laser scanning microscope in which the source of light was a diode laser of approximately 25 mW emitting radiation with a wavelength of approx. 405 nm. The thickness of the profile contact was determined on the basis of six median measurements.

2 RESULTS AND DISCUSSION

Based on the metallographic observations (Fig. 7), it was found that is not possible to apply silver pastes

to deposit front electrodes (about regular shape and uniform thickness) using EOSINT M 250 Xtended device with SLS technology. Fig. 8a presents test electrode system I deposited using PV145 paste and selectively laser sintered. The observations revealed the occurrence of grains of diversified size and shape, similar to the electrode structure from the same paste before its laser micro-treatment (Fig. 8b), total overbaking, and evaporation of the electrode with increasing laser beam power. Based on the graphs, which present the resistance - distance relationship for the contacts involving parameters: ρ_c (the specific contact resistance), L_T (a track of current impact), and R_c (contact resistance) (Fig. 9), the specific contact resistance defines not only the real joint zone of contact with the Si substrate, but the regions directly under and below surface of phase separation. The specific contact resistance of the front contact was calculated from formula in the literature, but other parameters like R_c and L_T were calculated from the linear regression. Contact resistance was measure using the TLM method, which consists of a direct current (I) measurement and voltage (U) measurement between any two separate contacts. In this paper, results were obtained for the specific contacts resistance of the front electrode for a given value of current (10, 30 and 50 mA) depending on the determined conditions of both the laser micro-treatment samples and the co-firing samples in the furnace. Fig. 9 presents an example of the series of test samples co-fired from the PV 145 paste in the temperature range of 830 to 920 °C onto a silicon solar cell with an ARC layer and without texture for a current value of 10 mA.

Based on the electrical properties investigations using the TLM method in the first series, it was found that the smallest specific contact resistance values of the test electrodes system I, II are respectively 0.17 to $0.57~\Omega\cdot\text{cm}^2$, 0.53 to $1.63~\Omega\cdot\text{cm}^2$ for solar cells with

different morphologies. The minimum value of the specific contact resistance was obtained for the test electrodes system I, II (0.17 $\Omega \cdot \text{cm}^2$; 0.53 $\Omega \cdot \text{cm}^2$) with a median thickness of 35 μm onto substrate without texture and without TiO_x coating using an applied laser beam of 37.8 W and a 50 mm/s feed rate of passage of the laser beam.

Based on the electrical properties investigations using the TLM method in the second series it was found that the smallest specific contact resistance of the test electrode system I, II are respectively 0.01 to 0.77 $\Omega \cdot \text{cm}^2$, 0.68 to 2.82 $\Omega \cdot \text{cm}^2$ for solar cells with different morphologies.

Table 6. Technical specification of Eosint M 250 Xtended device

Laser CO ₂	200 W
Feed rate of passage the laser beam	max. 3.0 m/s
Output laser beam	270 W
Diameter of laser beam	300 μm
Wavelength	10640 nm
Shielding gas	nitrogen

The minimum value of the specific contact resistance was obtained for the test electrodes system I (0.01 Ω ·cm², P = 51.3 W), II (0.68 Ω ·cm², P = 48.6 W) about median thickness 35 μ m onto the substrate without texture and without TiO_x coating with an applied feed rate of passage of the laser beam of 100 mm/s.

The thickness of the test electrodes was determined by checking the height profile of the three-dimensional surface topography measured using the confocal laser scanning microscope (Fig. 10).

Table 7. Conditions of co-firing in the furnace test electrodes systems I,II for silicon solar cells

Series	Paste	T	emperature [°0	[]
Selles	symbol	Zone I	Zone II	Zone III
				830
				860
1	B, C	530	570	890
				920
				945
2	PV 145	530	570 -	830
				860
				890
				920

Based on these series, it was found that silicon substrate morphology has a huge influence on obtaining a minimal resistance value of the electrodes selectively laser sintered from paste A. The minimal resistance value is greater for the substrate with texture than for the one without texture, which is probably connected to the occurrence of mpty areas under the contacts, since the median thickness of the pyramids' textured surface for Si (100) is in the range from 3 to 9 µm. The antireflection coating prevents the reflection of rays of sunshine and a loss of energy, as well as creating a barrier to the connection zone, what has an influence on increasing resistance between an electrode layer and a silicon substrate. The thickness of the deposited layer has an influence on the structure of the obtained electrode layer and the resistance value of the resistance electrode. It was found that SLS testing contacts with a median thickness of about 35 µm present both well adhering and condensing layers to the silicon substrate.

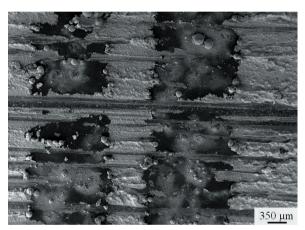


Fig. 7. Surface topography of front electrodes system I layer deposited from paste A on the silicon substrate and laser sintered where v = 100 mm/s and P = 32.4 W

Based on the electrical properties investigations using the TLM method it was found that the smallest specific contact resistance is obtained in a second series of test electrodes system I, II formed from the PV 145 paste. In the temperature range from 860 to 920 °C the minimum value of the specific contact resistance of the test electrode system I, II is equal to 1.08 to 2.73 $\Omega \cdot \text{cm}^2$; 0.48 to 1.33 $\Omega \cdot \text{cm}^2$ for solar cells with different morphologies. The highest range of specific contact resistance was found in the first series for front contacts made from C, PV 145 pastes. In the temperature range from 860 to 945 °C the minimum value of the specific contact resistance of test electrodes system I, II is equal to 1.83 to 86.81 Ω ·cm²; 1.12 to 71.63 Ω·cm² for solar cells with different morphologies. Electrical properties investigations using the TLM method confirm that the resistance paste decreases as the co-firing temperature of

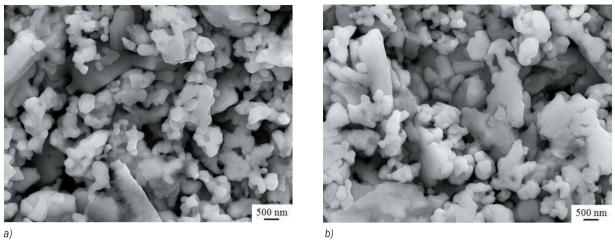


Fig. 8. Surface topography of the front electrode system I layer deposited by screen printing from PV 145 paste onto the silicon substrate:

a) not sintered, b) SLS with v=50 mm/s and P=32.4 W

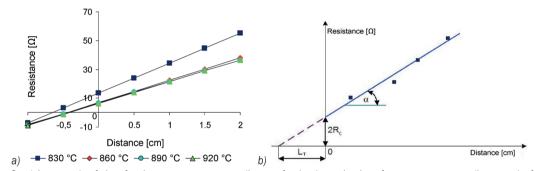


Fig. 9. a) An example of plot of resistance versus contact distance for the determination of contact parameters (L_T - a track of current impact) and Rc -contact resistance, slope= R_p/z , R_p - sheet resistance, Z - contact length); b) typical graphic method from the literature used to determine factors [9] and [10]

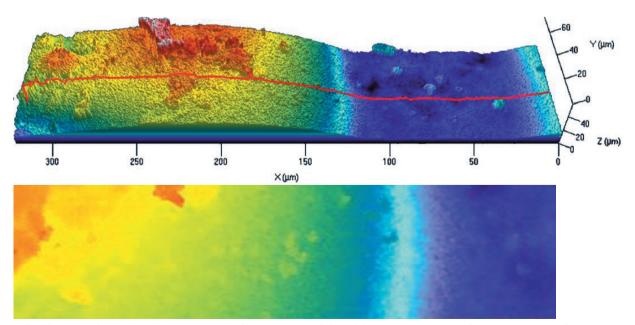


Fig. 10. Image of three- and two- dimensional surface topography (CLSM) of front electrode system I from paste A onto the Si substrate without texture or ARC layer by laser micro-machining

electrode increases, this is caused by the decreasing resistance connection between metallic grains. The contact resistance was found to grow with increasing temperature, which was probably caused by melted silver molecules [7], which formed huge agglomerates or where voids would develop under the contacts in textured surfaces.

The contact layers from the preformed PV 145 paste create many homogenous connections with the silicon substrate, while contact layers from the C, PV 145 pastes create point connections. The stability of electrical measurements using the TLM method guaranteed a median thickness of 35 μ m of the test electrodes system in the case of paste A, however in a case of the PV 145 paste this influence was not confirmed.

The thickness of the test electrodes co-fired at the highest temperatures with PV 145 and of the test electrodes sintered with a selective laser from the A paste decreases slightly versus the thickness of the electrodes before sintering. As a result of SEM investigations, the examined test front electrodes sintered using a selective laser reveal a diverse cross-section thickness of between 570 nm to 2.3 μ m.

The topographies of the silicon wafers with texture and an ARC layer were observed using an atomic force microscope.. The median thickness of the pyramids was determined by atomic force microscope and found to to be equal to 4 µm (Fig. 11).

The qualitative analysis of phase composition (Fig. 12) carried out using the X-ray diffraction method confirms that on the electrodes from sintered pastes, the layers contain the phase Ag, which was generated in congruence with the assumptions (Fig. 13). The presence of reflexes from the SI phase present in the substrate materials was demonstrated in the X-ray diffractograms obtained with the use of the Bragg-Brentano technique.

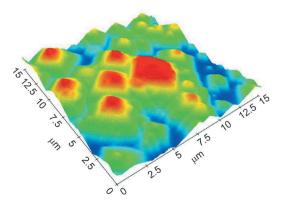
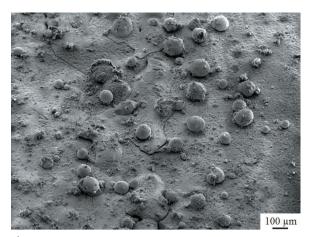
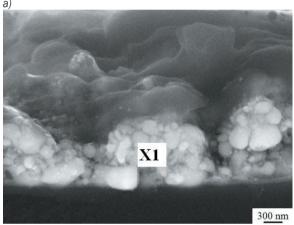


Fig. 11. The topography of monocrystalline silicon wafer with texture (an example)





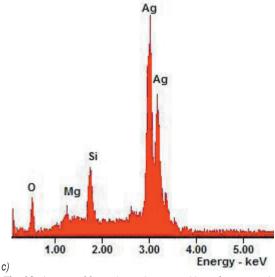


Fig. 12. Images of front electrode system I layer from paste A onto Si substrate without texture or ARC layer by laser micromachinining; a) topography image (SEM); b) fracture image (SEM); c) EDS spectra from X1 area

Fig. 13. X-ray diffraction pattern of front metallization performed from PV 145 paste onto

b)

silicon surface and co-fired in temperature 830 to 920 °C (where A means solar cells with different morphology)

3 SUMMARY

Based on the investigation results, the following optimal process parameters for manufacturing the front electrode of silicon solar cells were selected: selection for the composition of nano silver paste -83%Ag+2%SiO₂+15% organic carrier, selection of the median thickness of the deposited layer, laser microtreatment on two test systems - laser beam 37.8 W and feed rate of passage for the laser beam 50 mm/s. It was found that the silicon substrate morphology has a huge influence on obtaining a minimal resistance value of electrodes using both methods. It is bigger for the substrate with texture than for the one without texture, which is probably connected with the occurrence empty areas under contacts [8], since the median thickness of the pyramids' textured surface for Si (100) is in the range from 3 to 9 µm. The antireflection coating prevents the reflection of rays of sunshine and a loss of energy, as well creates a barrier into the connection zone, which increases the resistance between the electrode layer and the silicon substrate. The thickness of the deposited layer has an influence on the structure of the obtained electrode layer and resistance value of the resistance electrode. It was found that SLS testing of contacts with a median thickness of 35 µm presents both well adhering and condensing layers to the silicon substrate The contact layers performed from the PV 145 paste create many homogenous connections with the silicon substrate, while contact layers from the 85%Ag+15% organic carrier, 88.4%Ag+11.6% organic carrier pastes create point connections.

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